

# Review of a Time-variant bandpass filter for Fast Analog FEE

Pancha Yannick Hertz<sup>a</sup>, Wembe Tafo Evariste<sup>b,\*</sup>, Essimbi Zobo Bernard<sup>a</sup>

<sup>a</sup>University of Yaounde I, Faculty of sciences, Yaounde, PO Box 812 Yaounde, Cameroon <sup>b</sup>University of Douala, Douala, PO Box 2701 Douala, Cameroon

#### ABSTRACT

A time-variant circuit is examined in the context of readout electronics for silicon sensors. It consists of a time-variant differentiation and a two-stage integrator. To lower mean power consumption, the circuit can alternate between the on and off states. This will improve noise performance and restore output undershoot and offset.

Keywords: Integration, Time variant circuit, undershoot, Offset.

## I. INTRODUCTION

Medical imaging-based spectroscopy for positron emission tomography (PET) hugely utilizes hard X-rays and soft  $\gamma$ -rays provided by compound semiconductor detectors among which cadmium zinc telluride (CdZnTe). [ P. Yannick et al. (2021)]. For high resolution needs, low-noise readout electronics interfacing the sensors is crucial for accurate measurement of electric charges deposited by ionizing radiation and the arrival time of the incident photons. In the design of analog front-end electronics (FEE) for particle beam energies detection, the noise performance and DC baseline are optimises using customize circuits. Noise can quickly degrade system performance, especially at very low energy values. The filter must have strong selectivity to enhance this performance. For this, CR-RC<sup>n</sup> filters are used, however, they are prone to undershoot in high count rate applications. A pole zero cancellation circuit (PZC) is used to fix this undershoot, but the bandpass feature of the filter is lost [P Grybos et al (2008)]. The readout circuit is thus susceptible to low-frequency noise. In addition, CdZnTe detectors are subject to imperfections such as leakage current and shot noise. These various sources of flaws can create distortions in the signal by altering the operating point of the circuit. This signal can then be subject to a baseline drop that will degrade the energy resolution of FEE. In this work, we propose a correction circuit to solve the abovementioned problem while preserving the bandwidth characteristics of the circuit. The proposed circuit is based on time-variant circuit. It is a bandpass filter based on complementary switches, which increase noise performance by stabilizing the output baseline of the front-end circuit and restoring the bandpass nature of the circuit. As a result, it is possible to switch the output swing while maintaining the observed baseline and a specific voltage, with the discriminator threshold level in control. Results from post-layout simulation tests conducted in a 65 nm CMOS process from TSMC under 1.2 V supply voltage have confirmed the design.

#### II. PROPOSED CIRCUIT

The backbone filter circuit is a CR-RC<sup>2</sup> shaper, where a classical PZC is added along with a baseline restorer (BLR) module. The proposed circuit for undershooting cancellation and baseline restoration is based on a time-variant circuit called the TV-BLR and proposed by **Pancha et al. (2022)**.



### Figure 1: Time variant filter

The circuit is presented in figure 1 and operates in two modes. Firstly, when a charge is detected at the input, a signal is generated by the preamplifier and ends up at node A. Then, the TV-BLR circuit starts working. It consists of two switches and a discriminator. When the amplitude of the signal at node A is sufficiently large (above the threshold value), the discriminator generates a logic trigger significant as equivalent time over the threshold (ToT) signal **[T. Orita et al (2015)]**, large enough to toggle the control switches S<sub>1</sub> and S<sub>2</sub>. In this case, S<sub>1</sub> is closed, and S<sub>2</sub> is open, connecting the output to the upstream part of the circuit. The circuit formed by Cc, Roff, RC +Ron thus takes the configuration of a first-order high pass filter. This is referred as step 1. In the second step, the states of switches are permuted, connecting output node to the ground.

$$\begin{cases} \frac{V_{out}}{i_n} = H_s \frac{\tau_{csa}}{(1 + \tau_{csa}s)} \frac{(1 + \tau_z s)}{(1 + \tau_1 s)} \frac{\tau_2}{(1 + \tau_2 s)} \frac{\tau_3 s}{(1 + \tau_3 s)} \rightarrow step1 \quad (1) \\ \frac{V_{out}}{i_n} = 0 \rightarrow step2 \\ \end{cases}$$

$$\begin{cases} v_{out}(t) = H_s \tau_3 \tau_{csa} \frac{Q_{in}}{t_c} \left(\frac{t^2 e^{-t/\tau_2}}{2\tau_2^2}\right) + v_{noise}(t) \rightarrow Step1 \quad (2) \\ v_{out}(t) = 0 \rightarrow Step2 \end{cases}$$

Equations (1) and (2) are respectively in the transfer function and the time domain response of the circuit.

<sup>\*</sup> Corresponding author E-mail: twembee@yahoo.fr

## III. RESULTS OF SIMULATION

The time domain response of the circuit is depicted in the next figure 2. The preamplifier's integrated signal is boosted and the low-frequency noise components are removed throughout the integration time (step 1). The system's signal-to-noise ratio is decreased at this time due to the high amplitude gain. The circuit can process more interaction flow because the integration time is constrained to between 100 and 150 ns depending on the ToT the discriminator provides. Step 2 allows the system to be quickly reset for the next event after the event has been read. In situations where the discharge period is lengthy, the combination of these two processes not only assures that the baseline is always zero but also makes up for the energy lost as a result of undershooting.



#### Figure 2: Output response of TV filter circuit

According to equation 2, at stage 1, the signal amplitude increases while the temporal noise decreases. Additionally, in stage 2, the circuit noise is extremely similar to that of the ground, which is very little. As a result, the circuit's overall noise is significantly reduced when averaged across one period **[P. Y. Hertz et al. (2023)]**.

## IV. LIMITATIONS

This circuit can be very useful for baseline restoration and noise optimization. However, the output swing is limited to approximatively  $V_{dd} - V_{thn}$  due to the topology of the open loop amplifier. Hence, the input energy range is closed.

## V. FUTURE WORK

The future work is to improve the output swing of the circuit and to build step by step the entire FEE with the presented scheme. Otherwise, it will be helpful to improve the efficacy of the filter.

### References

- P. Yannick Hertz, F. Kamdem Jerome, N. Sidze Laure Vanessa, W. Tafo Evariste, E. Zobo Bernard, A PVT power immune compact 65 nm CMOS CSP design with a leakage current compensation feedback for CdZnTe/CdTe sensors dedicated to PET applications, International Journal of Circuit Theory and Applications 50 (6) (2022) 1817–33.
- P. Grybos, R. Szczygiel, Pole-Zero Cancellation Circuit With Pulse Pile-Up Tracking System for Low Noise Charge-Sensitive Amplifiers, IEEE Transactions on Nuclear Science 55 (1) (2008) 583–90.
- P. Y. Hertz, F. K. Jerome, W. T. Evariste, E. Z. Bernard, M. A. S. Bhuiyan, & M. H. Miraz, (2022, December). A TV-Module for DC-Baseline Compensation in Analog Front-End Design. In 2022 International Conference on Computing, Networking, Telecommunications & Engineering Sciences Applications (CoNTESA) (pp. 41-44). IEEE.
- Orita, T., Shimazoe, K., Takahashi, H., The dynamic time-over-threshold method for multi-channel APD based gamma-ray detectors, Nuclear Instruments and Methods in Physics Research Section A: Accelerators, Spectrometers, Detectors and Associated Equipment 775 (2015) 154–61.
- P.Y. Hertz, F.K. Jerome, Noumbissi S.L. Vanessa et al., CMOS readout FEE based TV-BLR module for CdZnTe pixel detectors in high count rate applications, Ain Shams Engineering Journal, https://doi.org/10.1016/j.asej.2023.102303